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Reliable memory design is one of the most challenging tasks in nano-scale CMOS technology due to the minimum or nearminimum sized devices and the high density requirement. This seminar presents various - state-of-the-art circuit techniques for embedded memories (SRAM, eDRAM, Logic-compatible eDRAM, and

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application of the nanoscale resistive switching memory devices in the memory landscape is derived. Finally, the suitability of the different device concepts for beyond pure memory applications, such as brain inspired and neuromorphic computational or logic in memory applications that strive to overcome the vanNeumann bottleneck, is discussed.

Nanoscale resistive switching memory devices: a review

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This review aims at discussing advances in nanotrapping memories, a term that is coined to embrace devices that functionally rely upon embedded nanoscale charge-trapping objects.

Nanotrapping memories - Nanoscale Horizons (RSC Publishing)

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